

In searching the Group I claims, the class and subclass for the Group II claims will undoubtedly be searched, to ensure that no relevant art is overlooked. For this reason there is no significant burden on the examiner, and certainly no serious burden as required by MPEP § 803. In fact, maintaining the requirement for restriction not only burdens applicants with the additional costs associated with filing and prosecuting separate patent applications, but also requires the examiner to duplicate efforts by examining multiple applications of closely related inventions. Such practice not only wastes public and private funds and Patent Office resources, but also leads to the possibility of inconsistent examinations of closely related inventions. Accordingly, applicants respectfully request that the examiner reconsider and withdraw the restriction requirement.

In light of the foregoing, applicants respectfully submit that a full and complete response to the office action is provided herein, and request that the application proceed to examination.

In the event this response is not timely filed, applicants hereby petition for the appropriate extension of time and request that the fee for the extension along with any other fees which may be due with respect to this paper be charged to deposit account 12-2252.

Sincerely,

LSI LOGIC CORPORATION



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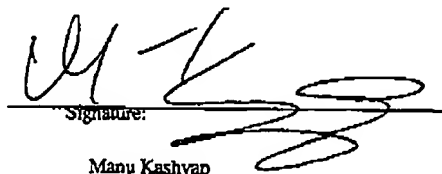
MAILING

- ☐ Deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

FACSIMILE

- ☒ Transmitted by facsimile to the Patent and Trademark Office.

Date: July 12, 2004



Signature:
Manu Kashyap